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4	4	((processor cpu microprocessor (processing adj unit)) with (associat$3 correspond$3) with (metric profil$3) with (sum total accumulat$3 execut$3)) same ((operating adj system) kernel)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/08 11:22
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3	121	(processor cpu microprocessor (processing adj unit)) with (associat$3 correspond$3) with (metric profil$3) with (sum total accumulat$3 execut$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/08 14:45
7	129	(processor cpu microprocessor (processing adj unit)) with (associat$3 correspond$3) with (metric profil$3 measur$5) with (sum total accumulat$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/08 14:54
8	93	((processor cpu microprocessor (processing adj unit)) with (associat$3 correspond$3) with (metric profil$3 measur$5) with (sum total accumulat$3)) not ((processor cpu microprocessor (processing adj unit)) with (associat$3 correspond$3) with (metric profil$3) with (sum total accumulat$3 execut$3))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/08 14:54
9	11	(processor cpu microprocessor (processing adj unit)) with (associat$3 correspond$3) with (metric profil$3 measur$5) with (sum total accumulat$3) with (execut$3 compil$3 run$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/08 15:51
11	467	(metric profil$3) with (processor cpu microprocessor (processing adj unit)) with level$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/08 15:37
12	22	((metric profil$3) with (processor cpu microprocessor (processing adj unit)) with level$3) same (kernel (operating adj system))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/08 15:38
13	43	((metric profil$3 measur$6) with (processor cpu microprocessor (processing adj unit)) with level$3) same (kernel (operating adj system))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/08 15:38
14	21	(processor cpu microprocessor (processing adj unit)) with (associat$3 correspond$3) with (metric profil$3 measur$5) with (chang$3 modif$7) with (execut$3 compil$3 run$4 load$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/08 15:55

15	2	"6055492" .pn.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/08 15:55
16	3269	(processor cpu microprocessor (processing adj unit)) near3 (metric profil$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/08 16:09
17	1	(smp multiprocessing) near3 (processor cpu microprocessor (processing adj unit)) near3 (metric profil$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/08 16:10
18	210	(second multiple plurality multi) near3 (processor cpu microprocessor (processing adj unit)) near3 (metric profil$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/08 16:11
19	3	(second multiple plurality multi) near3 (processor cpu microprocessor (processing adj unit)) near3 (associat$3 correspond$3) near3 (metric profil$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/08 16:11

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1 An analysis of operating system behavior on a simultaneous multithreaded architecture
Joshua A. Redstone, Susan J. Eggers, Henry M. Levy
November 2000 **Proceedings of the ninth international conference on Architectural support for programming languages and operating systems**, Volume 28, 34 Issue 5, 5

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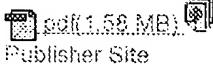
This paper presents the first analysis of operating system execution on a simultaneous multithreaded (SMT) processor. While SMT has been studied extensively over the past 6 years, previous research has focused entirely on user-mode execution. However, many of the applications most amenable to multithreading technologies spend a significant fraction of their time in kernel code. A full understanding of the behavior of such workloads therefore requires execution and measurement of the operating sy ...

2 Performance characterization of a Quad Pentium Pro SMP using OLTP workloads
Kimberly Keeton, David A. Patterson, Yong Qiang He, Roger C. Raphael, Walter E. Baker
April 1998   **ACM SIGARCH Computer Architecture News , Proceedings of the 25th annual international symposium on Computer architecture**, Volume 26 Issue 3

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Commercial applications are an important, yet often overlooked, workload with significantly different characteristics from technical workloads. The potential impact of these differences is that computers optimized for technical workloads may not provide good performance for commercial applications, and these applications may not fully exploit advances in processor design. To evaluate these issues, we use hardware counters to measure architectural features of a four-processor Pentium Pro-based se ...

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